

WHAT IS CLAIMED IS:

1. A method comprising:
  - for a first thread, entering a processing queue for obtaining permission to enter a critical section of code;
  - determining if a second thread exists, the second thread executing the critical section of code concurrently with the first thread entering the processing queue; and
  - if the second thread exists, then determining if the second thread is executing the critical section;
  - if the second thread is executing the critical section, then testing for the second thread to complete until one of the following occurrences:
    - the second thread completes; and
    - the yielding count expires.
2. The method of claim 1, additionally comprising if the yielding count expires before the second thread completes, then exiting the processing queue.
3. The method of claim 2 additionally comprising re-entering the processing queue after a period of time.
4. The method of claim 3, wherein the period of time is determined by an operating system scheduling algorithm.
5. The method of claim 1, additionally comprising if the second thread completes before the yielding count expires, then executing the first critical section of code.
6. The method of claim 1, additionally comprising if the second thread does not exist, then executing the first critical section of code.
7. The method of claim 1, wherein the yielding count is based on the number



- 1 13. The method of claim 11, wherein if the yielding count expires before the  
2 second thread completes, then exiting the processing queue.
- 1 14. The method of claim 13, additionally comprising re-entering the  
2 processing queue after a determined amount of time.
- 1 15. A method comprising:
- 2 a. initializing shared variables, the shared variables including a  
3 turn variable, a first status flag, and a second status flag;
- 4 b. reading the shared variables into a memory;
- 5 c. entering a processing queue;
- 6 d. determining if a yield count has expired;
- 7 e. if the yield count has expired, then exiting the processing queue;
- 8 f. if the yield count has not expired, then for a contending process,  
9 determining if a concurrent process exists;
- 10 g. retrieving the second status flag and the turn variable from the  
11 memory, reading the status flag into a first cache and reading  
12 the turn variable into the second cache to determine if the  
13 concurrent process is executing a critical section of code;
- 14 h. if the concurrent process is not executing a critical section of  
15 code, then entering the critical section of code, and upon  
16 completing the critical section of code, resetting the first status  
17 flag; and;
- 18 i. if the concurrent process is executing the critical section of  
19 code, then repeating d through i.

1

16. The method of claim 15, wherein the second cache is larger than the first cache.

17. The method of claim 15, wherein resetting the first status flag comprises retrieving a reset value from a register.

18. A machine-readable medium having stored thereon data representing sequences of instructions, the sequences of instructions which, when executed by a processor, cause the processor to perform the following:

for a first thread, enter a processing queue for obtaining permission to enter a critical section of code;

determine if a second thread exists, the second thread executing the critical section of code concurrently with the first thread entering the processing queue; and

if the second thread exists, then determine if the second thread is executing the critical section;

if the second thread is executing the critical section, then test for the second thread to complete until one of the following occurrences:

the second thread completes; and

the yielding count expires.

19. The method of claim 18, additionally comprising if the yielding count expires before the second thread completes, then exiting the processing queue.

20. The method of claim 18, additionally comprising if the second thread does not exist, then executing the first critical section of code.

21. An apparatus comprising:

at least one processor;

a machine-readable medium having instructions encoded thereon, which when executed by the processor, are capable of directing the processor to:

for a first thread, enter a processing queue for obtaining permission to enter a critical section of code;

determine if a second thread exists, the second thread executing the critical section of code concurrently with the first thread entering the processing queue; and

if the second thread exists, then test for the second thread to complete until one of the following occurrences:

the second thread completes; and

the yielding count expires.

22. The method of claim 21, additionally comprising if the yielding count expires before the second thread completes, then exiting the processing queue.

23. The method of claim 21, additionally comprising if the second thread does not exist, then executing the first critical section of code.

24. An apparatus comprising:

means for a first thread to enter a processing queue for obtaining permission to enter a critical section of code;

means to determine if a second thread exists, the second thread executing the critical section of code concurrently with the first thread entering the processing queue; and

if the second thread exists, then means to determine if the second thread is executing the critical section;

9 if the second thread is executing the critical section, then means to test for  
10 the second thread to complete until one of the following  
11 occurrences:

12 the second thread completes; and

13 the yielding count expires.

1 25. The method of claim 24, additionally comprising if the yielding count  
2 expires before the second thread completes, then exiting the processing  
3 queue.

1 26. The method of claim 25 additionally comprising re-entering the processing  
2 queue after a period of time.

1 27. The method of claim 26, wherein the period of time is determined by an  
2 operating system scheduling algorithm.

1 28. The method of claim 24, additionally comprising if the second thread  
2 completes before the yielding count expires, then executing the first critical  
3 section of code.

1 29. The method of claim 24, additionally comprising if the second thread does  
2 not exist, then executing the first critical section of code.

1 30. The method of claim 24, wherein the yielding count is based on the  
2 number of threads contending to enter a corresponding critical section of  
3 code.

1